Amendments to the Specification:

Please replace the paragraph beginning at page 5, line 21, following the heading "Brief description of the drawings", and ending at page 6, line 8, with the following amended paragraph:

Fig. 1a shows a first example of embodiments of a storage system; Fig. 1b shows an example of a second embodiment of a storage system; Fig. 2 shows one configuration example of a FIFO buffer used in the storage system; Fig. 3 shows a third example of the embodiments of the storage system according to the present invention; Fig. 4 shows one configuration example of a FIFO buffer with ~~buttery~~ a battery used in the storage system; Fig. 5 shows one example of an appearance of a mother board of a controller used in the storage system; Fig. 6 shows one embodiment of a method of speeding up writing data into the storage system; and Fig. 7 shows a fourth example of the embodiments of the storage system.

Please replace the paragraph beginning at page 6, line 13, and ending at page 7, line 4, with the following amended paragraph:

Fig. 1a is a diagram showing a first embodiment of a storage system to which the present invention is applied. The storage system has two control units (hereinafter referred to as controllers) 10, two power units 11, and a plurality of storage devices (hereinafter referred to as disk drives) 12. As used herein, the disk drives 12 are devices obtainable by using a storage such as a hard disk and an optical disk. Each of

the disk drives 12 is connected to both of the controllers 10. The power units 11a and 11b are independent from each other, wherein the power unit 11a feeds power to the controller 10a and the power unit 11b feeds power to the controller 10b. Thus, if one of the power units 11 is at fault, the storage system can continue its operation by feeding power to the controllers 10 by the use of the other one of the power units 11.

Please replace the paragraph beginning at page 8, line 13, and ending at page 8, line 19, with the following amended paragraph:

Hereinafter, a data writing process in the storage system of Fig. 1a will be described briefly. For example, when data sent from the host system connected to the controller 10a are to be written into the storage system, the data is received first by the host interface unit 100a. Note that the similar processing is performed when the data is received by the controller 10b, too.

Please replace the paragraph beginning at page 10, line 18, and ending at page 11, line 3, with the following amended paragraph:

By the use of a CRC (cyclic redundancy check) or the like, the data checker 305a checks whether or not the data sent from the host system are erroneous due to failure that might have occurred during the transfer. When the data checker 305a confirms that there is no error, the result is sent to the write controller 302a via a signal line 311a. Upon receipt of the result from the data checker 305a, the write controller

302a outputs a write signal via a signal line 313a. Upon receipt of the write signal, the FIFO memory 304a stores the data transferred from the host system.

Please replace the paragraph beginning at page 13, line 13, and ending at page 13, line 23, with the following amended paragraph:

A storage system of the present invention may have the following second embodiment, which has power units 43a and 43b in addition to the constitution of the first embodiment. In the storage system illustrated in Fig. 1b, ~~a~~ FIFO buffer ~~402a~~102b is connected to the power unit 43b and ~~a~~ FIFO buffer ~~402a~~102a is connected to the power unit 43a. With the constitution of the second embodiment, the power units 43a and 43b serve only to feed power respectively to the FIFO buffers 102a and 102b; therefore, it is possible to reduce capacities of the power units as compared with those of the power units 11a and 11b.